

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S62 7	608	clear\$3 with bios	US-PGPUB; USPAT	OR	ON	2004/10/29 12:52
S62 8	220	(clear\$3 with bios) and detecting	US-PGPUB; USPAT	OR	ON	2004/10/29 12:52
S62 9	26	((clear\$3 with bios) and detecting) and latching	US-PGPUB; USPAT	OR	ON	2004/10/29 12:55
S63 0	113	(clear\$3 with bios) and "713"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2004/10/29 12:56
S63 1	9	clear\$3 adj bios	US-PGPUB; USPAT	OR	OFF	2004/10/29 14:05
S63 2	80	clear with bios	US-PGPUB; USPAT	OR	OFF	2004/10/29 14:05
S63 3	33	bios adj clear\$3	US-PGPUB; USPAT	OR	OFF	2004/11/01 13:56
S63 4	13	clear adj cmos	US-PGPUB; USPAT	OR	OFF	2004/11/01 13:56
S63 5	83	clear near (bios or cmos)	US-PGPUB; USPAT	OR	ON	2004/11/01 15:34
S63 6	1970	reset with (bios or cmos)	US-PGPUB; USPAT	OR	ON	2004/11/02 09:44
S63 7	1903	reset with (bios or cmos)	US-PGPUB; USPAT	OR	OFF	2004/11/02 09:45
S63 8	1598	(reset with (bios or cmos)) and @ad<"20020621"	US-PGPUB; USPAT	OR	OFF	2004/11/02 09:46
S63 9	588	((reset with (bios or cmos)) and @ad<"20020621") and latch\$	US-PGPUB; USPAT	OR	OFF	2004/11/02 09:47
S64 0	435	((reset with (bios or cmos)) and @ad<"20020621") and latch\$) and detect\$	US-PGPUB; USPAT	OR	OFF	2004/11/02 09:47
S64 1	82	((reset with (bios or cmos)) and @ad<"20020621") and latch\$) and detect\$) and "713"/\$.ccls.	US-PGPUB; USPAT	OR	OFF	2004/11/02 10:08
S64 2	214	bios .ti.	US-PGPUB; USPAT	OR	OFF	2004/11/02 10:08
S64 3	182	(bios .ti.) and @ad<"20020621"	US-PGPUB; USPAT	OR	OFF	2004/11/02 11:03
S64 4	4030	cmos .ti.	US-PGPUB; USPAT	OR	OFF	2004/11/02 11:04
S64 5	3551	(cmos .ti.) and @ad<"20020621"	US-PGPUB; USPAT	OR	OFF	2004/11/02 11:04
S64 6	925	(cmos .ti.) and (clear or reset)	US-PGPUB; USPAT	OR	OFF	2004/11/02 11:04
S64 7	360	(cmos .ti.) and ((clear or reset) .clm.)	US-PGPUB; USPAT	OR	OFF	2004/11/02 11:05

S64 8	263	((cmos .ti.) and ((clear or reset) . clm.)) and @ad<"20020621"	US-PGPUB; USPAT	OR	OFF	2004/11/02 12:49
S64 9	2346	memory adj clear\$	US-PGPUB; USPAT	OR	OFF	2004/11/02 12:49
S65 0	107	(memory adj clear\$) and "713"/\$. ccls.	US-PGPUB; USPAT	OR	OFF	2004/11/02 15:01
S65 1	2188	(clear or reset) with (bios or cmos)	US-PGPUB; USPAT	OR	OFF	2004/11/02 15:02
S65 2	486	((clear or reset) with (bios or cmos)) with signal	US-PGPUB; USPAT	OR	OFF	2004/11/02 15:02
S65 3	121	((clear or reset) with (bios or cmos)) with latch	US-PGPUB; USPAT	OR	OFF	2004/11/02 15:05
S65 4	109	((clear or reset) with (bios or cmos)) with latch) and @ad<"20020621"	US-PGPUB; USPAT	OR	OFF	2004/11/02 15:07
S65 5	9	(clear or reset) with (cmos or bios)	IBM_TDB	OR	OFF	2004/11/02 15:09
S65 6	680	(clear or reset) near3 (cmos or bios)	US-PGPUB; USPAT	OR	OFF	2004/11/02 15:10
S65 7	598	((clear or reset) near3 (cmos or bios)) and @ad<"20020621"	US-PGPUB; USPAT	OR	OFF	2004/11/02 15:10
S65 8	454	(clear or reset) near2 (cmos or bios)	US-PGPUB; USPAT	OR	OFF	2004/11/02 15:10
S65 9	439	(clear or reset) adj3 (cmos or bios)	US-PGPUB; USPAT	OR	OFF	2004/11/02 15:10
S66 0	176	(clear or reset) adj (cmos or bios)	US-PGPUB; USPAT	OR	OFF	2004/11/02 15:19
S66 1	17	clear adj (cmos or bios)	US-PGPUB; USPAT	OR	OFF	2004/11/02 15:19
S66 2	180	jumper with latch	US-PGPUB; USPAT	OR	ON	2004/11/03 09:18
S66 3	175	S662 and @ad<"20020621"	US-PGPUB; USPAT	OR	ON	2004/11/03 09:14
S66 4	5	S662 and "713"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2004/11/03 09:15
S66 5	102	jumper adj position	US-PGPUB; USPAT	OR	ON	2004/11/03 09:19
S66 6	0	S665 with (remember or detect)	US-PGPUB; USPAT	OR	ON	2004/11/03 09:18
S66 7	4	S665 and "713"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2004/11/03 09:24
S66 8	157	jumper with (remember or detect)	US-PGPUB; USPAT	OR	ON	2004/11/03 09:24
S66 9	9	S668 and "713"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2004/11/03 09:42

S67 0	15436	jumper	US-PGPUB; USPAT	OR	ON	2004/11/03 09:42
S67 1	721232	S670 and erase or clear	US-PGPUB; USPAT	OR	ON	2004/11/03 09:42
S67 2	4272	S670 and (erase or clear)	US-PGPUB; USPAT	OR	ON	2004/11/03 09:43
S67 3	130	S670 with (erase or clear)	US-PGPUB; USPAT	OR	ON	2004/11/03 11:16
S67 4	10	S673 and "713"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2004/11/03 09:43
S67 5	37	jumper with latching	US-PGPUB; USPAT	OR	ON	2004/11/03 12:38
S67 6	1	("6006327").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2004/11/03 12:43
S67 7	2	((("4796211") or ("5862351"))).PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2004/11/03 12:43
S67 8	14	("4967346" "5086501" "5371892" "5594874" "5675794" "5754833").PN. OR ("5862351"). URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2004/11/03 13:29
S67 9	0	("9847975").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2004/11/03 13:29
S68 0	0	("9847975.an.").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2004/11/03 13:29
S68 1	0	"09847975".an.	US-PGPUB; USPAT	OR	ON	2004/11/03 13:38
S68 2	15436	jumper	US-PGPUB; USPAT	OR	ON	2004/11/03 13:38
S68 3	469	S682 and "713"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2004/11/03 13:38
S68 4	415	S683 and @ad<"20020621"	US-PGPUB; USPAT	OR	ON	2004/11/03 13:39
S68 5	832	S682 and motherboard	US-PGPUB; USPAT	OR	ON	2004/11/03 13:39
S68 6	241	S684 and detect	US-PGPUB; USPAT	OR	ON	2004/11/03 13:40
S68 7	2162	jumper	EPO; JPO	OR	ON	2004/11/03 13:53
S68 8	17	S687 with (detect or remember)	EPO; JPO	OR	ON	2004/11/03 14:00
S68 9	0	jumper adj position	EPO; JPO	OR	ON	2004/11/03 14:37

S69 0	1	jumper with motherboard	EPO; JPO	OR	ON	2004/11/03 14:38
S69 1	1	jumper and motherboard	EPO; JPO	OR	ON	2004/11/03 14:38
S69 2	1996	switch adj position	US-PGPUB; USPAT	OR	ON	2004/11/04 09:30
S69 3	259	S692 with detect	US-PGPUB; USPAT	OR	ON	2004/11/04 09:30
S69 4	4	S693 and "713"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2004/11/04 09:53
S69 5	39	completely adj clear	EPO; JPO	OR	ON	2004/11/04 09:57
S69 6	4078	completely adj clear	US-PGPUB; USPAT	OR	ON	2004/11/04 09:57
S69 7	15	S696 and "713"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2004/11/04 10:06
S69 8	13	S696 with memory	US-PGPUB; USPAT	OR	ON	2004/11/04 12:01
S69 9	4	((("6549966") or ("6081856") or ("6438708") or ("6334160"))).PN.	US-PGPUB; USPAT	OR	OFF	2004/11/04 15:54
S70 0	56	(state or status) near jumper	USPAT	OR	OFF	2004/11/04 15:55
S70 1	96	(state or status) near2 jumper	USPAT	OR	OFF	2004/11/04 15:55
S70 2	5	(stor\$3 or latch\$3 or sav\$3 or remember\$3) with S701	USPAT	OR	OFF	2004/11/04 16:04
S70 3	1172	jumper with (board or motherboard)	USPAT	OR	OFF	2004/11/04 16:04
S70 4	5	S703 with latch	USPAT	OR	OFF	2004/11/04 16:05
S70 5	461	jumper with (remember\$3 or detect\$3)	USPAT	OR	OFF	2004/11/04 16:06
S70 6	0	S705 and g06f1/\$.ipc.	US-PGPUB; USPAT	OR	ON	2004/11/04 16:08
S70 7	53	S705 and g06f\$/\$.ipc.	US-PGPUB; USPAT	OR	ON	2004/11/04 16:08
S72 5	47	latch\$3 with chipset	US-PGPUB; USPAT	OR	ON	2004/11/09 15:08